SIEGET®45 BFP520F



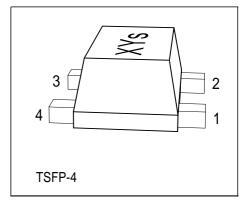
NPN Silicon RF Transistor

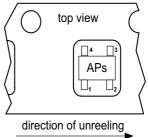
Preliminary data

 For highest gain low noise amplifier at 1.8 GHz and 2 mA / 2 V

Outstanding $G_{ms} = 23 \text{ dB}$ Noise Figure F = 0.95 dB

- For oscillators up to 15 GHz
- Transition frequency $f_T = 45 \text{ GHz}$
- Gold metallization for high reliability
- SIEGET ® 45 Line 45 GHz f_T - Line





ESD: Electrostatic discharge sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration				Package
BFP520F	APs	1 = B	2 = E	3 = C	4 = E	TSFP-4

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}	2.5	V
Collector-base voltage	$V_{\rm CBO}$	10	
Emitter-base voltage	V _{EBO}	1	
Collector current	l _C	40	mA
Base current	l _B	4	
Total power dissipation	P _{tot}	100	mW
<i>T</i> _S ≤ 107°C			
Junction temperature	T _j	150	°C
Ambient temperature	T _A	-65 150	
Storage temperature	T _{stg}	-65 150	

Thermal Resistance

Junction - soldering point ¹⁾	R_{thJS}	≤ 430	K/W

 $^{^{1}}$ For calculation of R_{thJA} please refer to Application Note Thermal Resistance



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified.

Parameter	Symbol	Values			Unit	
		min.	typ.	max.	1	
DC characteristics	•		!	!		
Collector-emitter breakdown voltage	V _{(BR)CEO}	2.5	3	3.5	V	
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$						
Collector-base cutoff current	I _{CBO}	-	-	200	nA	
$V_{CB} = 5 \text{ V}, I_{E} = 0$						
Emitter-base cutoff current	I _{EBO}	-	-	35	μA	
$V_{EB} = 1 \text{ V}, I_{C} = 0$						
DC current gain	h _{FE}	70	110	200	-	
$I_{\rm C} = 20 \text{ mA}, \ V_{\rm CE} = 2 \text{ V}$						
AC characteristics (verified by random sampling	g)					
Transition frequency	f _T	-	45	-	GHz	
$I_{\rm C} = 30 \text{ mA}, \ V_{\rm CE} = 2 \text{ V}, \ f = 2 \text{ GHz}$						
Collector-base capacitance	C _{cb}	-	0.07	-	pF	
$V_{CB} = 2 \text{ V}, f = 1 \text{ MHz}$						
Collector-emitter capacitance	C _{ce}	-	0.25	-		
$V_{CE} = 2 \text{ V}, f = 1 \text{ MHz}$						
Emitter-base capacitance	C _{eb}	-	0.31	-		
$V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}$						
Noise figure	F	-	0.95	-	dB	
$I_{\text{C}} = 2 \text{ mA}, \ V_{\text{CE}} = 2 \text{ V}, \ Z_{\text{S}} = Z_{\text{Sopt}},$						
f = 1.8 GHz						
Power gain, maximum stable ¹⁾	G _{ms}	-	23	-		
$I_{C} = 20 \text{ mA}, V_{CE} = 2 \text{ V}, Z_{S} = Z_{Sopt}, Z_{L} = Z_{Lopt},$						
f = 1.8 GHz						
Insertion power gain	$ S_{21} ^2$	-	20.5	-	dB	
$I_{\rm C} = 20 \text{ mA}, \ V_{\rm CE} = 2 \text{ V}, \ f = 1.8 \text{ GHz},$						
$Z_{\rm S} = Z_{\rm L} = 50\Omega$						
Third order intercept point at output ²⁾	IP ₃	-	23.5	-	dBn	
$V_{CE} = 2 \text{ V}, f = 1.8 \text{ GHz}, Z_{S} = Z_{L} = 50\Omega,$						
$I_{\rm C}$ = 20 mA						
1dB compression point ³⁾	P _{-1dB}	-	10.5	-		
$V_{CE} = 2 \text{ V}, f = 1.8 \text{ GHz}, Z_{S} = Z_{L} = 50\Omega,$						
$I_{\rm C}$ = 20 mA						

 $^{{}^{1}}G_{ms} = |S_{21} / S_{12}|$

 $^{^2}$ IP3 value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50Ω from 0.1MHz to 6GHz.

³DC current at no input power

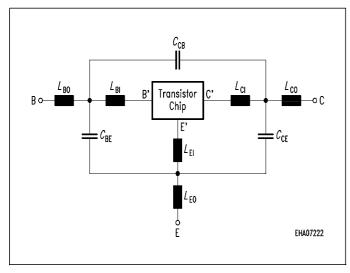


SPICE Parameters (Gummel-Poon Model, Berkley-SPICE 2G.6 Syntax):

Transistor Chip Data

IS =	15	aA	BF =	235	-	NF =	1	-
VAF =	25	V	IKF =	0.4	Α	ISE =	25	fA
NE =	2	-	BR =	1.5	-	NR =	1	-
VAR =	2	V	IKR =	0.01	Α	ISC =	20	fA
NC =	2	-	RB =	11	Ω	IRB =	-	Α
RBM =	7.5	Ω	RE =	0.6		RC =	7.6	Ω
CJE =	235	fF	VJE =	0.958	V	MJE =	0.335	-
TF =	1.7	ps	XTF =	10	-	VTF =	5	V
ITF =	0.7	mA	PTF =	50	deg	CJC =	93	fF
VJC =	0.661	V	MJC =	0.236	-	XCJC =	1	-
TR =	50	ns	CJS =	0	fF	VJS =	0.75	V
MJS =	0.333	-	XTB =	-0.25	-	EG =	1.11	eV
XTI =	0.035	-	FC =	0.5	-	TNOM	298	K

Package Equivalent Circuit:



$$L_{\rm BO} = 0.22$$
 nH $L_{\rm BI} = 0.42$ nH $L_{\rm EO} = 0.28$ nH $R_{\rm LBI} = 0.15$ Ω $L_{\rm CO} = 0.22$ nH $L_{\rm EI} = 0.26$ nH KBO-EO = 0.10 - $R_{\rm LEI} = 0.11$ Ω KBO-CO = 0.01 - $L_{\rm CI} = 0.35$ nH KEO-CO = 0.11 - $R_{\rm LCI} = 0.13$ Ω $C_{\rm BE} = 34$ fF KCI-EI = -0.05 - $C_{\rm BC} = 2$ fF KBI-CI = -0.08 - Valid up to 6GHz

The TSFP-4 package has two emitter leads. To avoid high complexity of the package equivalent circuit, both leads are combined in one electrical connection.

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 $R_{\rm LXI}$ are series resistors for the inductances $L_{\rm XI}$ and $K_{\rm Xa-yb}$ are the coupling coefficients between the inductances $L_{\rm Xa}$ and $L_{\rm Vb}$. The referencepins for the coupled ports are B, E, C, B`, E`, C`.

For examples and ready to use parameters please contact your local Infineon Technologies distributor or sales office to obtain a Infineon Technologies CD-ROM or see Internet: http://www.infineon.com/silicondiscretes